

FIG.1(a)

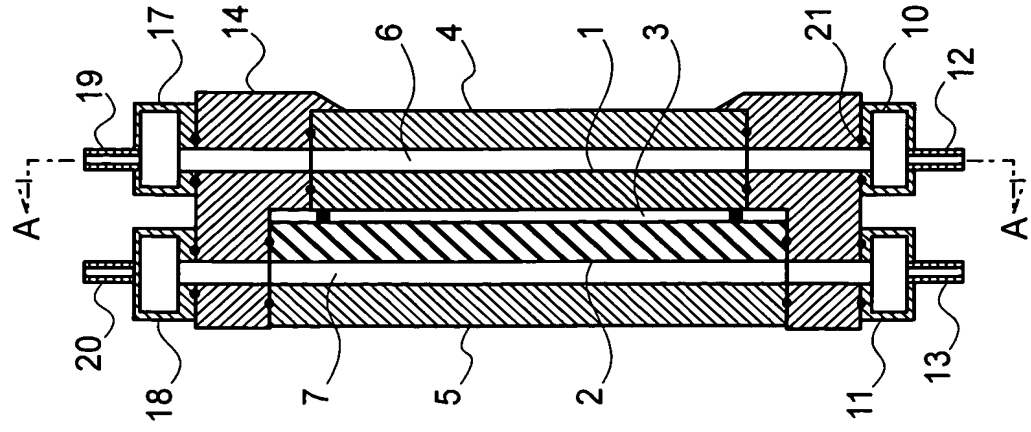


FIG.1(b)

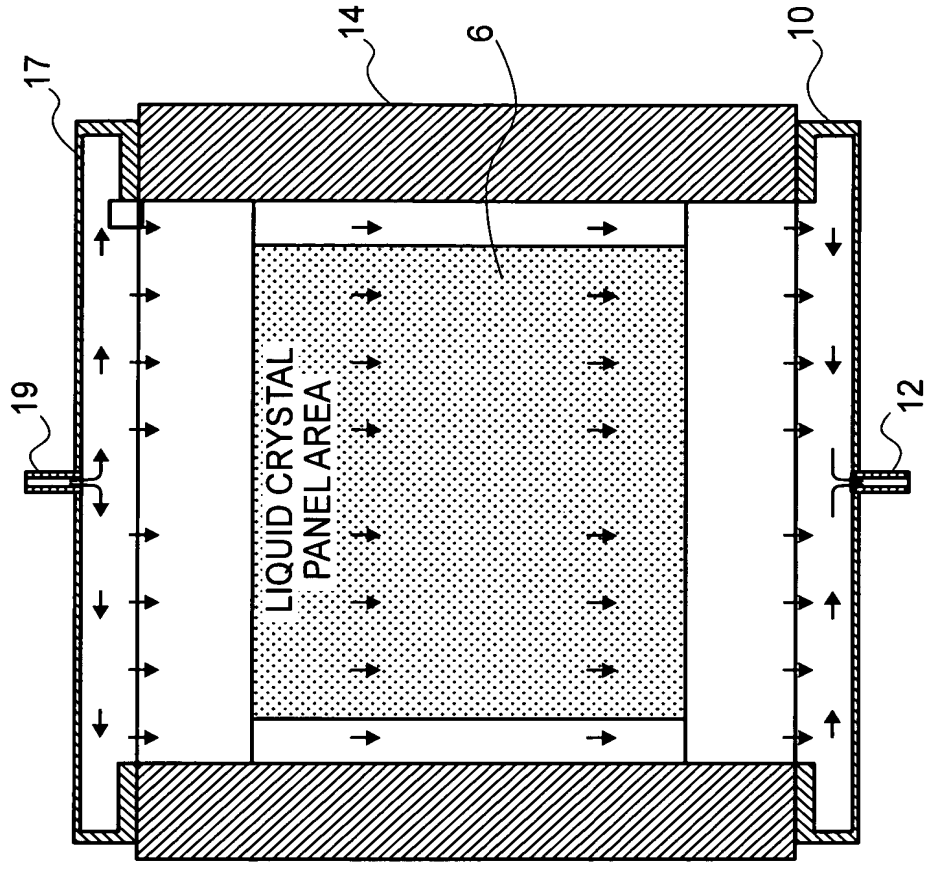


FIG.2

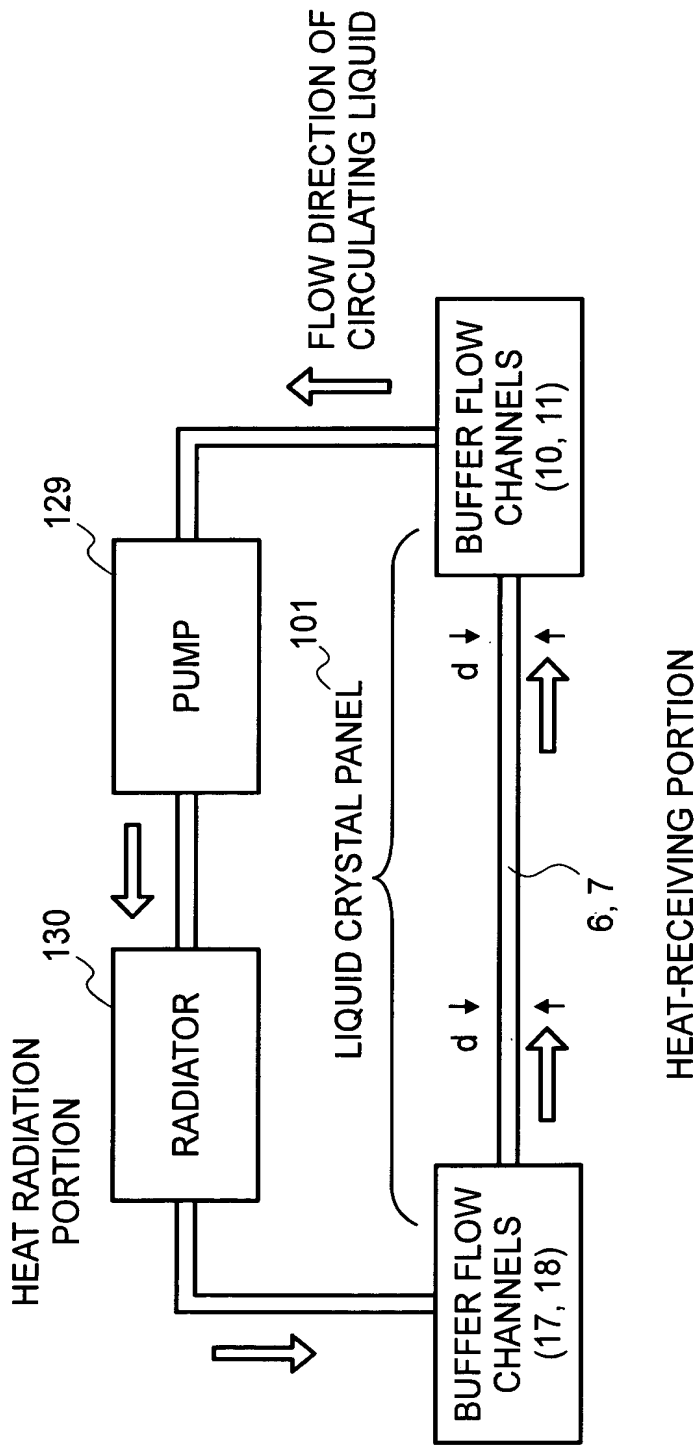
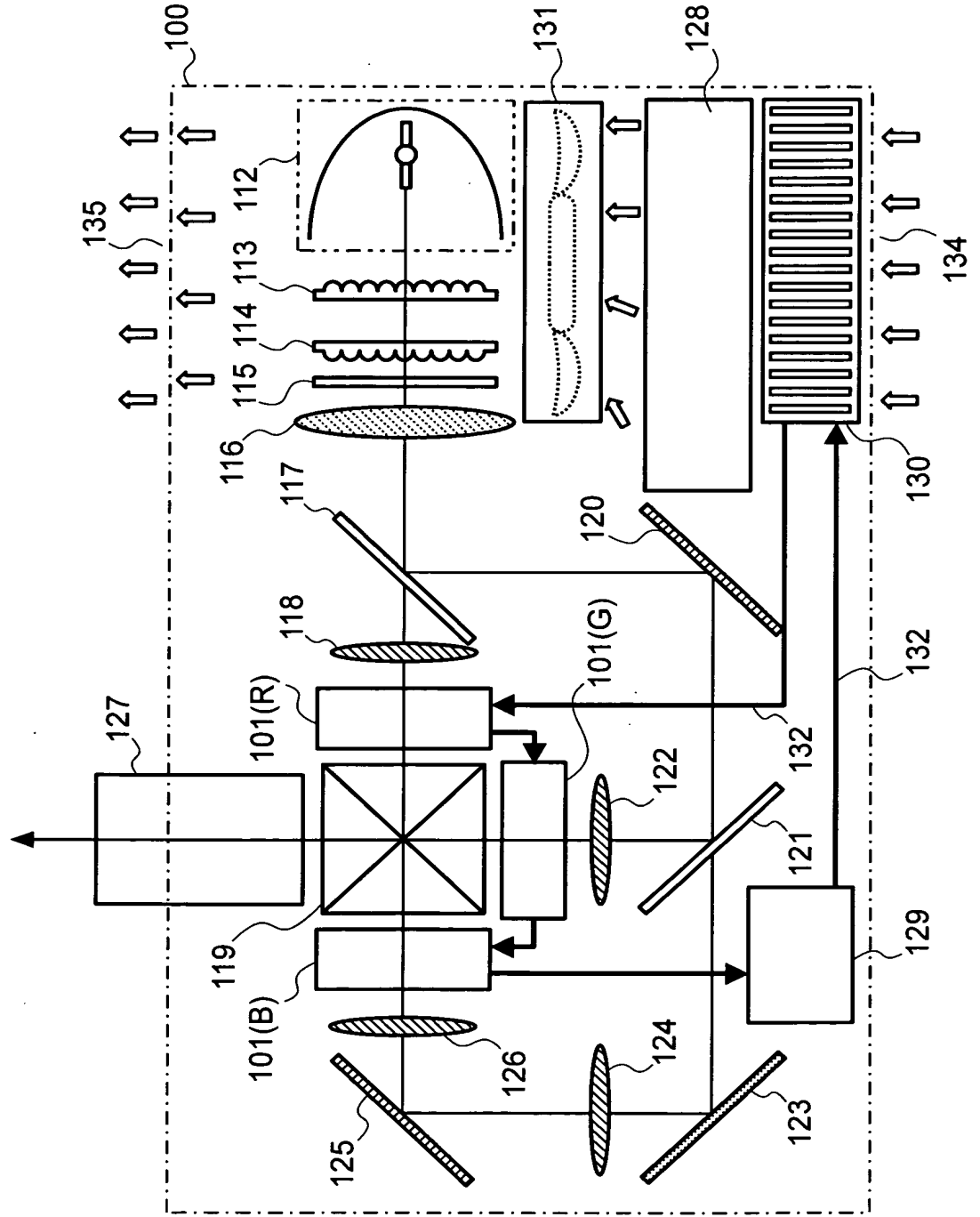
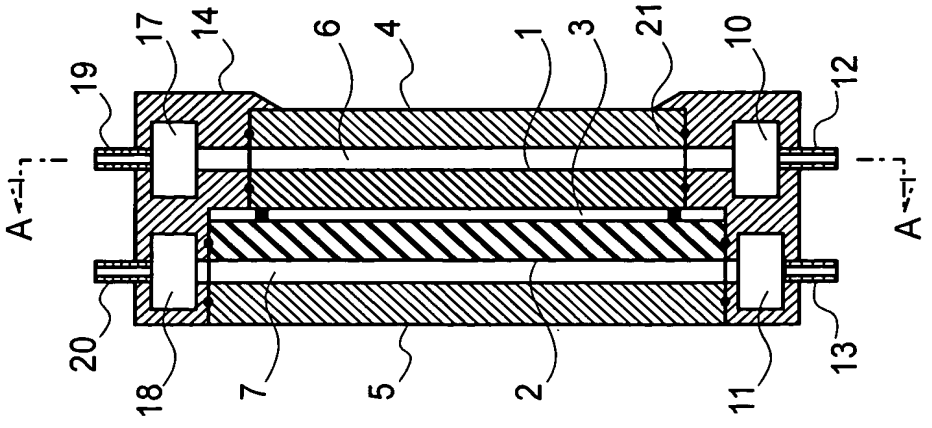


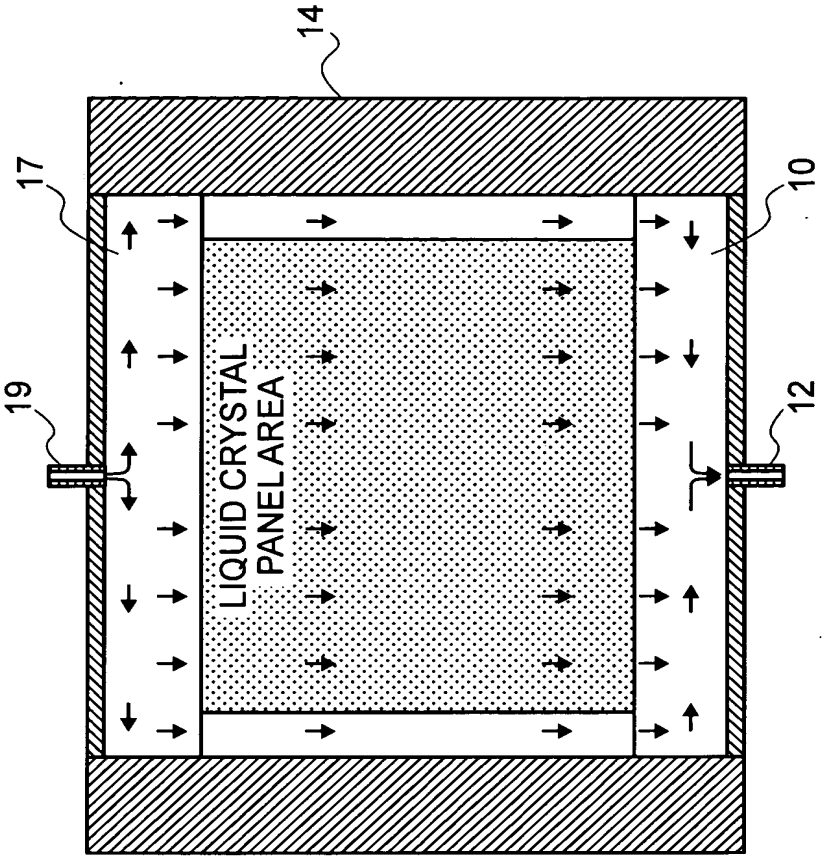
FIG.3



**FIG. 4(a)**



**FIG. 4(b)**



A cross-sectional view of a liquid crystal panel assembly. The assembly consists of a top substrate (14) and a bottom substrate (10), both shown with diagonal hatching. Between them is a liquid crystal layer (60) represented by a stippled pattern. The text "LIQUID CRYSTAL PANEL AREA" is written vertically within this layer. On the left side, a vertical channel (17) is formed between the substrates, containing a liquid crystal sealant (19) at the bottom. Arrows indicate the flow of liquid crystal from the sealant into the panel area. On the right side, a similar channel (12) is shown with a sealant (12) and arrows indicating flow into the panel area. A central vertical channel (14) is also depicted with arrows pointing downwards, likely for venting or filling.

FIG.6(a)

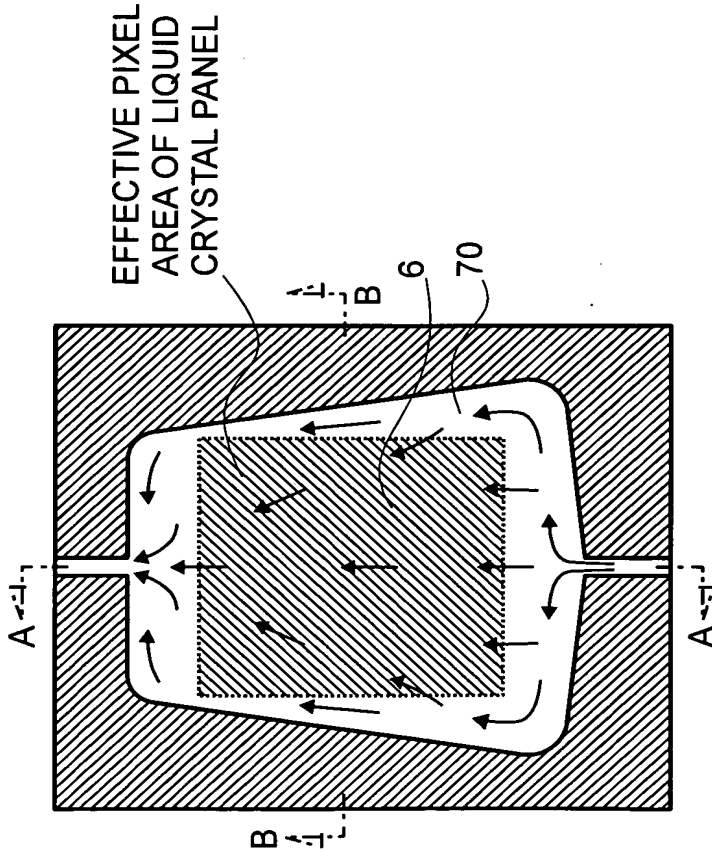


FIG.6(b)

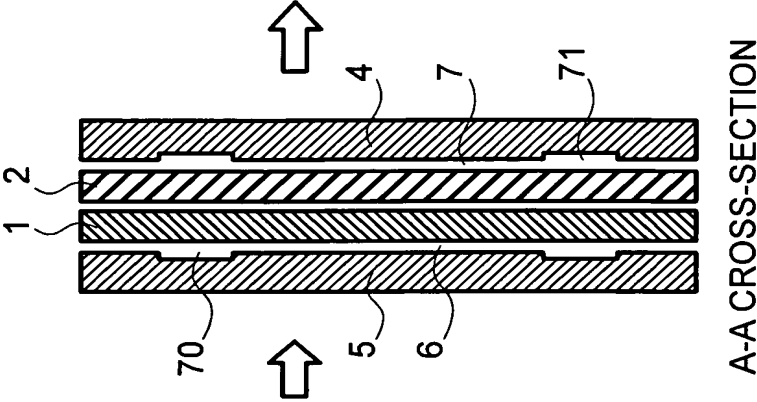
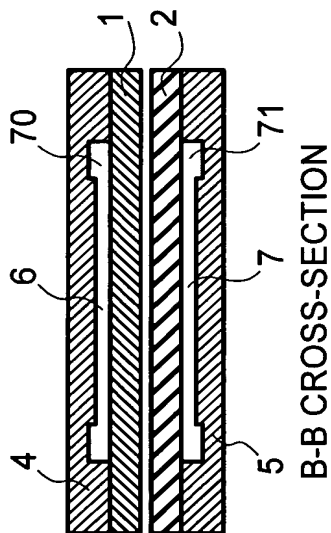


FIG.6(c)



**FIG.7**

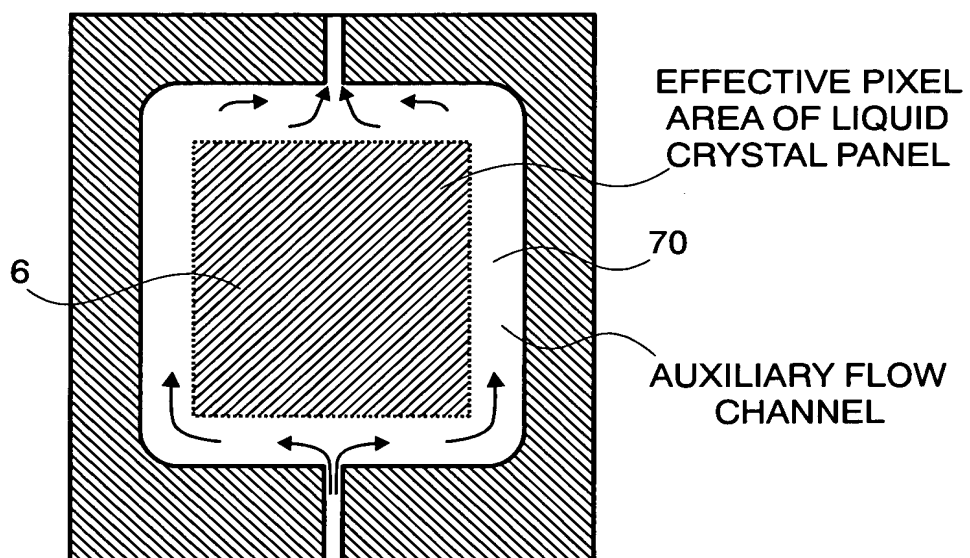


FIG.8(a)

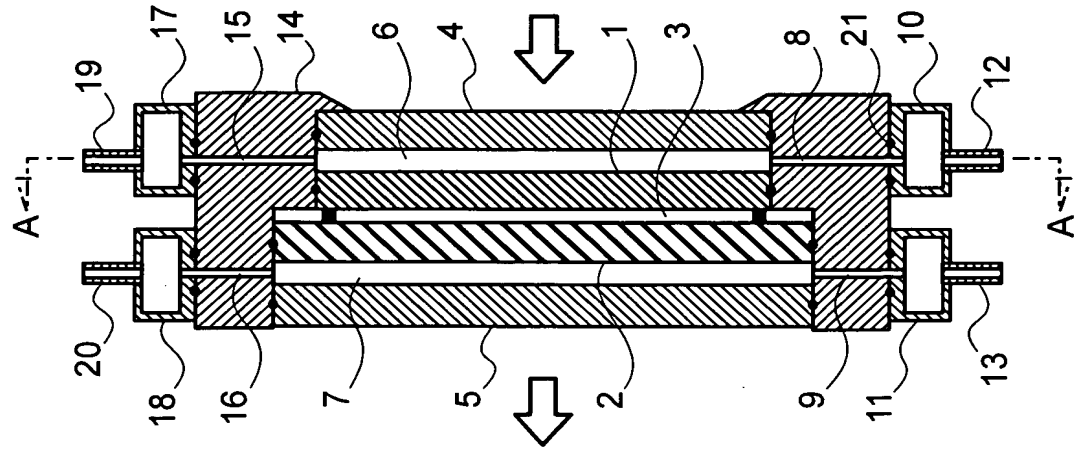
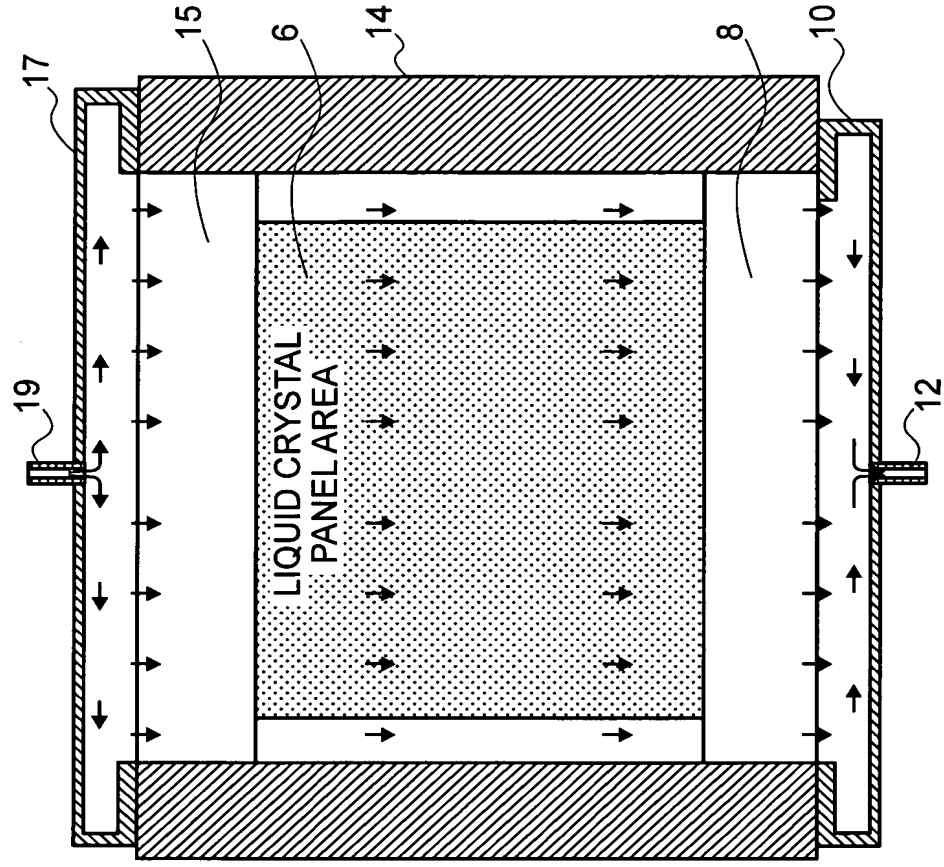
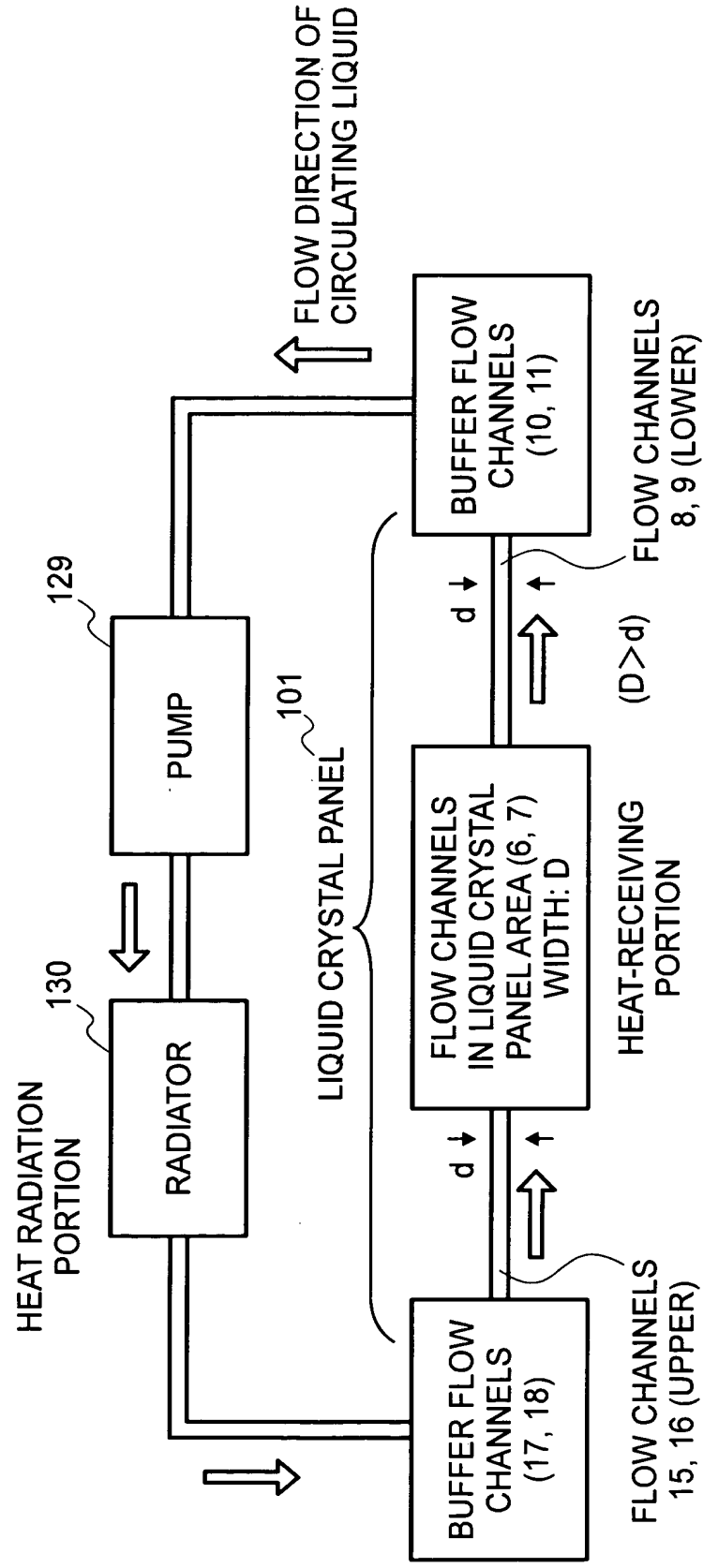


FIG.8(b)





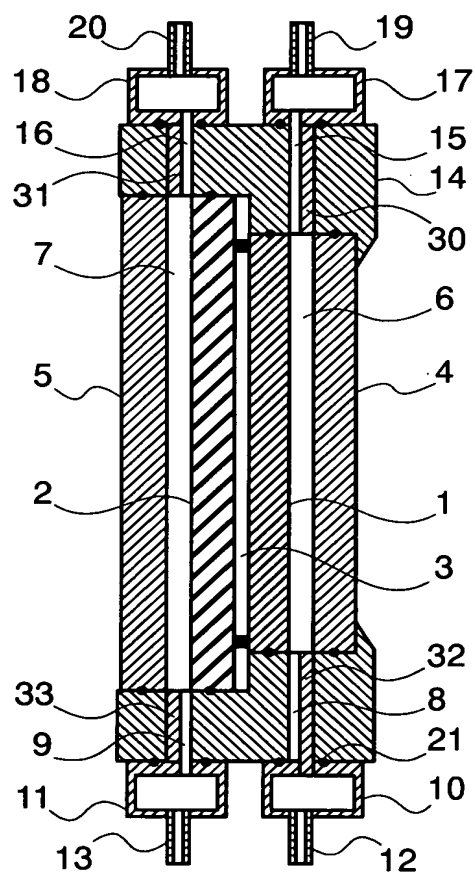
**FIG.9**



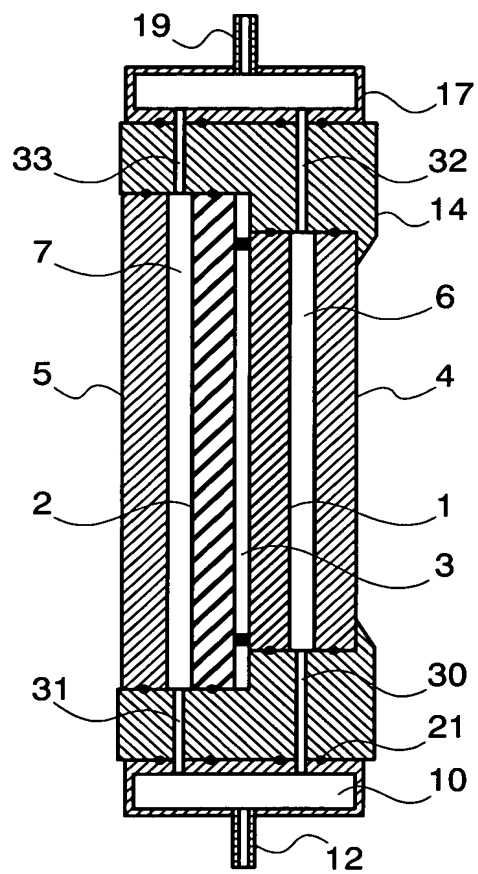
This cross-sectional diagram shows the internal components of the device. It features two main cylindrical chambers, 1 and 2, separated by a central partition 3. Chamber 1 contains a piston rod 6 connected to a piston 7. Chamber 2 contains a piston rod 5 connected to a piston 8. The pistons are seated within their respective chambers, which have seals or O-rings 9 and 10. The entire assembly is housed within a casing 11. Various ports and connections are labeled with numbers 1 through 13, including inlet/outlet ports 12 and 13, and internal passages 14, 15, 16, 17, 18, 19, and 20. Section lines A-A and B-B indicate the planes for the cross-sections shown.

This diagram shows a cross-sectional view of a liquid crystal display assembly. It features a central **LIQUID CRYSTAL PANEL AREA** (indicated by a dashed line) and a surrounding **PIXEL AREA**. The assembly is bounded by a top substrate (10) and a bottom substrate (30). A liquid crystal layer (14) is positioned between the substrates. A series of vertical electrodes (32) are located within the pixel area. On the left and right sides, there are input/output ports (17 and 19 on the left, 12 and 16 on the right) with associated wiring. Arrows indicate the flow of liquid crystal material or electrical signals through the various layers and components.

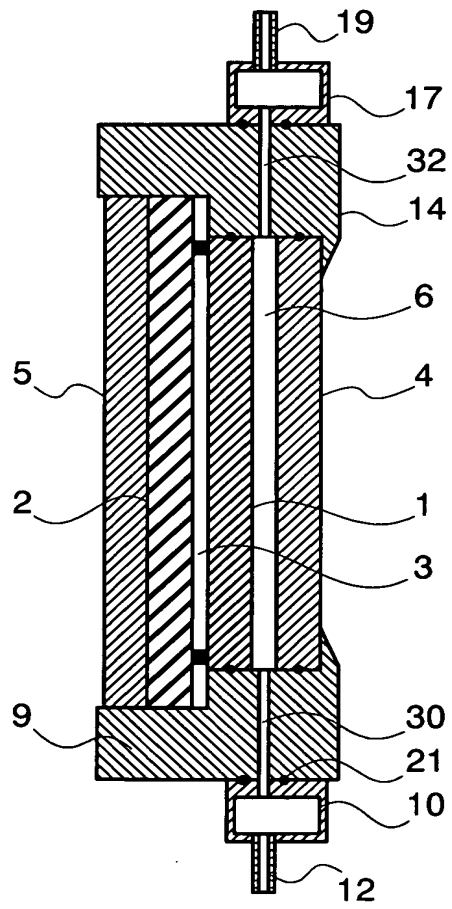
**FIG.11**



**FIG.12**



**FIG.13**



**FIG.14**

